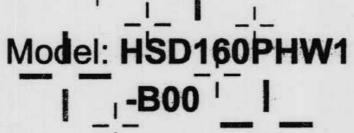
HannStar

Document Title	HSD160PHW1 Specification for Wealth Star	Page No.	1/26
Document No.	DC130-004854	Revision	1.0

TO: Wealth Star Electronic Limited

Date: 2010/6/29

Customer Acceptance Specification



Proposed by: Technical Service Division

Signature Date

Note: (1) Please contact HannStar Display Corp. before designing your product based on this module specification.

- (2) The information contained herein is presented merely to indicate the characteristics and performance of our products. No responsibility is assumed by HannStar for any intellectual property claims or other problems that may result from application based on the module described herein.
- (3) The mark " ** " of Model means sub-model code.

Document Title	HSD160PHW1 Specification for Wealth Star	Page No.	2/26
Document No.	DC130-004854	Revision	1.0

	Record of Revisions				
Rev.	Rev. Date Sub-Model Description of change				
	2010/6/29	B00	Formal Product Specification was first released.		



Document Title	HSD160PHW1 Specification for Wealth Star	Page No.	3/26
Document No.	DC130-004854	Revision	1.0

Contents

1.0	General description	p.4
2.0	Absolute maximum ratings	p.5
3.0	Optical characteristics	p.6
4.0	Block diagram	p.10
5.0	Interface pin connection	p.12
6.0	Electrical characteristics	p.13
7.0	Reliability test items	p.20
8.0	Outline dimension	p.21
9.0	Lot mark	p.23
10.0	Package specification	p.24
11.0	General precaution	p.25

Document Title	HSD160PHW1 Specification for Wealth Star	Page No.	4/26
Document No.	DC130-004854	Revision	1.0

1.0 GENERAL DESCRIPTION

1.1 Introduction

HannStar Display model HSD160PHW1-B is a color active matrix thin film transistor (TFT) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT LCD panel, a driving circuit and a back light system. This TFT LCD has a 16 (16:9) inch diagonally measured active display area with HD (1366 horizontal by 768 vertical pixel) resolution.

1.2 Features

- 16 (16:9 diagonal) inch configuration
- One channel LVDS interface
- 262K color by 6 bit R.G.B signal input
- RoHS Compliance
- Halogen Free

1.3 Applications

■ NB

1.4 General information

Item		Specification	Unit
Outline Dimension	on	365 x 214 x 5.5 (Typ.)	mm
Display area		353.45(H) x 198.72(V)	mm
Number of Pixel		1366 RGB (H) x 768(V)	pixels
Pixel pitch		0.2587(H) x 0.2587(V)	mm
Pixel arrangement		RGB Vertical stripe	
Display mode		Normally white	
NTSC		60	%
Surface treatment		Glare, Hard-Coating (3H)	
Weight		450 (Typ.)	g
Back-light		White LED	
Power Consumption	Logic System	1.2 (Max.) L0 pattern	



Document Title	HSD160PHW1 Specification for Wealth Star	Page No.	5/26
Document No.	DC130-004854	Revision	1.0

1.5 Mechanical Information

Item		Min.	Тур.	Max.	Unit
Module	Horizontal (H)	364.5	365	365.5	mm
Size	Vertical (V)	213.5	214	214.5	mm
Oize	Depth (D)	_	5.5	5.8	mm
Weight		_	450	465	g

2.0 ABSOLUTE MAXIMUM RATINGS

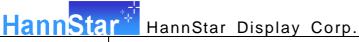
2.1 Electrical Absolute Rating

2.1.1 TFT LCD Module

Item	Symbol	Min.	Max.	Unit	Note
Logic Supply voltage	V_{DD}	-0.3	4.0	>	

2.1.2 Environment Absolute Rating

Item	Symbol	Min.	Max.	Unit	Note
Operating Temperature	T_{opa}	0	50	$^{\circ}\!\mathbb{C}$	
Storage Temperature	T_{stg}	-20	60	$^{\circ}\mathbb{C}$	



Document Title	HSD160PHW1 Specification for Wealth Star	Page No.	6/26
Document No.	DC130-004854	Revision	1.0

3.0 OPTICAL CHARACTERISTICS

3.1 Optical specification

3.1 Optical s	pecificat	1011	1			ı	1	1
Item		Symbol	Condition	Min.	Тур.	Max.	Unit	Note
Contrast		CR		480	600	_		(1)(2)(4)
Rising		Tr			2.4	4.8		(4) (0)
Response time	Falling	Tf			5.6	11.2	msec	(1)(3)
White luminand (5 point)	ce	Y _L	⊖=0	176	220	_	cd/m ²	(1)(4)(5) (I _L =15mA)
		R _x	Normal	0.584	0.614	0.644		
	Red	R _Y	viewing	0.312	0.342	0.372		
		G _x	angle	0.318	0.348	0.378		
Color	Green	G _Y		0.582	0.612	0.642		
chromaticity	Blue	B _x		0.120	0.150	0.180		
(CIE1931)		B _Y		0.039	0.069	0.099		
		W _x		0.283	0.313	0.343		
	White	W_y		0.299	0.329	0.359		
	بر ما ا	Θ_{L}		40	45	_		
Viewine e en ele	Hor.	Θ_{R}	CD. 40	40	45	_		(4)(4)
Viewing angle	\	θυ	CR>10	10	15	5 _	(1)(4)	
	Ver. ⊖ _D	θρ		30	35	_		
Brightness uniformity		B _{UNI}	⊖=0 (5point)	80	_	_	%	(6)
Brightness Uni	formity	B _{UNI}	⊖=0 (13 points)	70	_	_	%	(6)

3.2 Measuring Condition

■ Measuring surrounding : dark room
 ■ LED current I_L : 15mA / single LED
 ■ Ambient temperature : 25±2°C

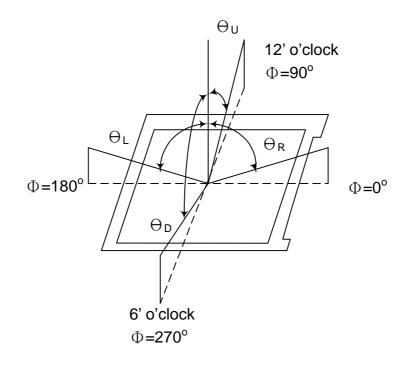
■ 15min. warm-up time.



Document Title	HSD160PHW1 Specification for Wealth Star	Page No.	7/26
Document No.	DC130-004854	Revision	1.0

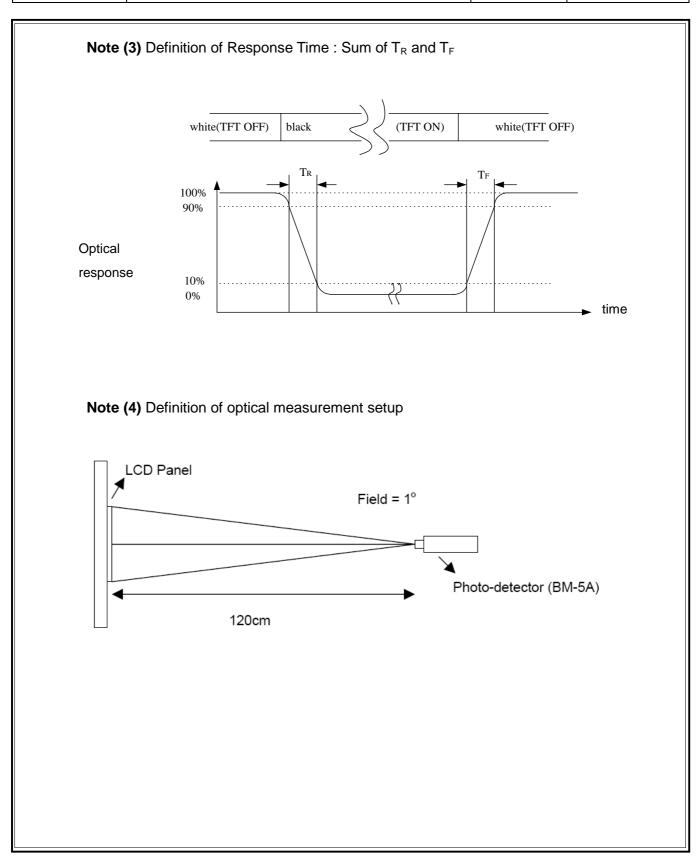
3.3 Measuring Equipment

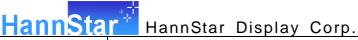
- FPM520 of Westar Display technologies, INC., which utilized SR-3 for Chromaticity and BM-5A for other optical characteristics.
- Measuring spot size : 20 ~ 21 mm Note (1) Definition of Viewing Angle:



Note (2) Definition of Contrast Ratio (CR) : measured at the center point of panel

Document Title	HSD160PHW1 Specification for Wealth Star	Page No.	8/26
Document No.	DC130-004854	Revision	1.0

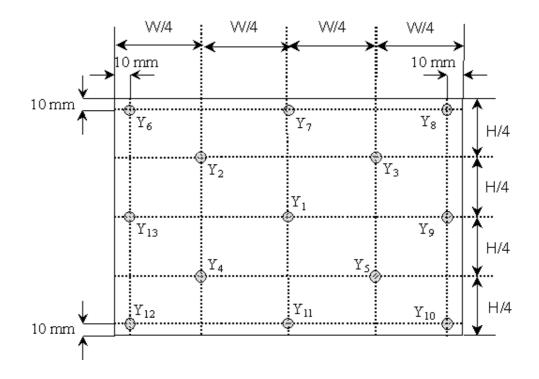




Document Title	HSD160PHW1 Specification for Wealth Star	Page No.	9/26
Document No.	DC130-004854	Revision	1.0

Note (5) Definition of Average Luminance Uniformity of White (5 Point)

Average Luminance Uniformity =
$$\frac{Y_1+Y_2+Y_3+Y_4+Y_5}{5}$$



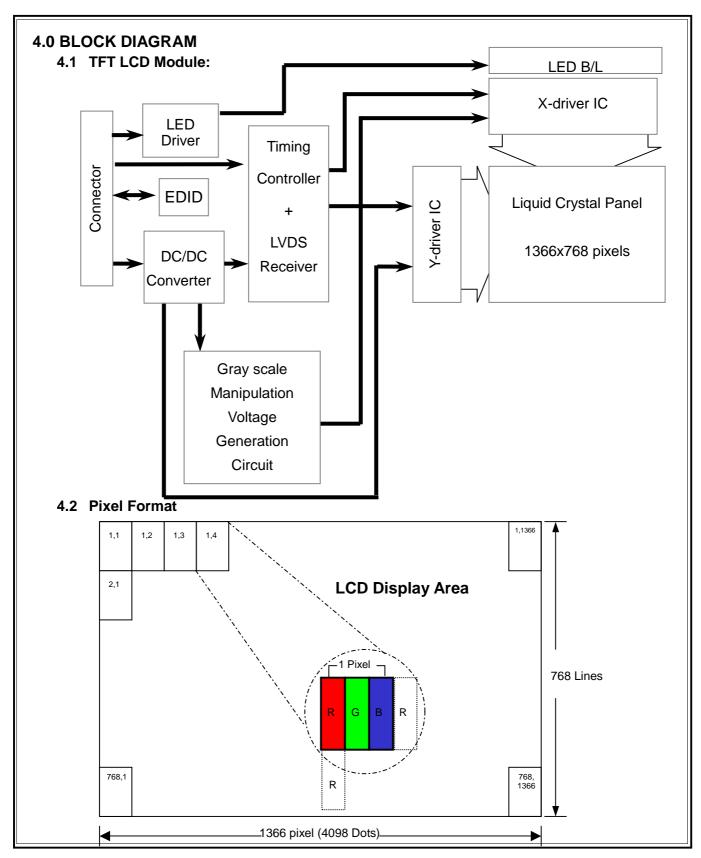
Note (6) Definition of brightness uniformity

Luminance uniformity(5 points) =
$$\frac{\text{(Min Luminance of 5 points)}}{\text{(Max Luminance of 5 points)}} \times 100\%$$

Luminance uniformity(13points) =
$$\frac{\text{(Min Luminance of 13 points)}}{\text{(Max Luminance of 13 points)}} \times 100\%$$

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Document Title	HSD160PHW1 Specification for Wealth Star	Page No.	10/26
Document No.	DC130-004854	Revision	1.0



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Document Title	HSD160PHW1 Specification for Wealth Star	Page No.	11/26
Document No.	DC130-004854	Revision	1.0

4.3 Relationship Between Displayed Color and Input MSB LSB MSB LSB MSB LSB Gray scale R1 R0 G5 G4 R 5 R4 R3 R2 G3 G2G1 G0B5 B4 В2 В1 Display В3 B0 level Black LIL Blue L ı LH Н Н Н Н Η Green LH Н Н Н Н HL L Basic Light Blue L LH Η Η Н Η HH Η Н Η Н Н color Red Н Н Н HL LL L Purple Н Н Н HL LH Н Н Н Н Н Yellow Н Η Η Η H|HН Η L L L L L White Н Н Н Η Н H|HΗ Н Н Н HH Н Н Н Н Н Black L L L L LL L L L L LL L L L L L L0 L L ΗЦ L L L L LL L L L1 Dark Gray scale L3...L60 of Red Light Η Н Н Н L ΗL L L L L LL L L L L L L61 Г LL L Η Н Н Η L L L L L L L L62 Red Н I Red L63 Н Black L L LL L0 H|IL1 LL LL L2 L Dark Gray scale of L3...L60 Green Light LH Н Н H|IL61 Н L L LH Н Η Н LL L L L62 LH Н L Green L63 Green L 1 L L Н Н Н H|IL L L L Black L LL L L0 L L L L L L L Η L1 Dark Gray scale of L3...L60 Blue Light L LI L L LH Н Н Н L Н L61 LH L Н Н Н Н L62 Blue LΗ Η L L L Н Н Н Н Blue L63 Black L L L LIL L L0 L L L L Н L L HL L L Н L1 Н L2 Gray Dark scale of L3...L60 White & Black Light Н Н HH Н Н Н НН L61 Н Η Η LH Н Н Н LH Н Н Н L L62 White Н Н Н White L63 Η Η Η H|HΗ Η Η Η Η Η Η Η

Document Title	HSD160PHW1 Specification for Wealth Star	Page No.	12/26
Document No.	DC130-004854	Revision	1.0

5.0 INTERFACE PIN CONNECTION

5.1 TFT LCD Module: **CN1** (Input signal): IPEX 20455-040E-12 (IPEX or equivalent)

Pin No.	Signal	Description
1	NC	Not connection
2	AVDD	Power Supply, 3.3V (typical)
3	AVDD	Power Supply, 3.3V (typical)
4	DVDD	DDC 3.3V power
5	NC	No Connection
6	SCL	EDID Clock
7	SDA	EDID Data
8	Rin0-	- LVDS differential data input (R0-R5,G0)
9	Rin0+	+LVDS differential data input (R0-R5,G0)
10	GND	Ground
11	Rin1-	- LVDS differential data input(G1-G5,B0-B1)
12	Rin1+	+LVDS differential data input (G1-G5,B0-B1)
13	GND	Ground
14	Rin2-	- LVDS differential data input (B2-B5,HS,VS,DE)
15	Rin2+	+LVDS differential data input (B2-B5,HS,VS,DE)
16	GND	Ground
17	CIKIN-	-LVDS differential clock input
18	CIKIN+	+LVDS differential clock input
19	GND	Ground
20	NC	No Connection
21	NC	No Connection
22	GND	Ground-Shield
23	NC	No Connection
24	NC	No Connection
25	GND	Ground-Shield
26	NC	No Connection
27	NC	No Connection
28	GND	Ground-Shield
29	NC	No Connection
30	NC	No Connection
31	VLED_GND	LED Ground
32	VLED_GND	LED Ground
33	VLED_GND	LED Ground
34	NC	No Connection
35	PWM	PWM Signal for LED dimming control
36	LED_EN	LED Enable Pin (+3V Input)
37	NC	No Connection
38	VLED	LED Power Supply 6-20V
39	VLED	LED Power Supply 6-20V
33		

Note: The brightness of LCD panel could be changed by adjusting PWM



Document Title	HSD160PHW1 Specification for Wealth Star	Page No.	13/26
Document No.	DC130-004854	Revision	1.0

6.0 ELECTRICAL CHARACTERISTICS

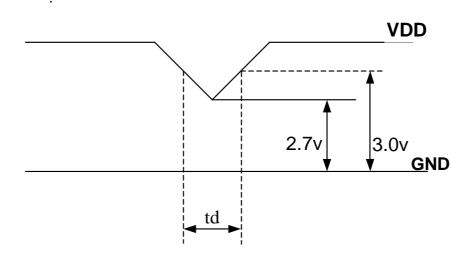
6.1 TFT LCD Module

Item	Symbol	Min.	Тур.	Max.	Unit	Note
Supply Voltage	V_{DD}	3.0	3.3	3.6	V	Note (1)
Current of power supply	IDD	-	0.3	-	Α	V _{DD} =3.3V ⋅ L0 pattern
Inrush current	I _{RUSH}	-	-	1.50	Α	Note (2)

Note (1): V_{DD}-dip condition:

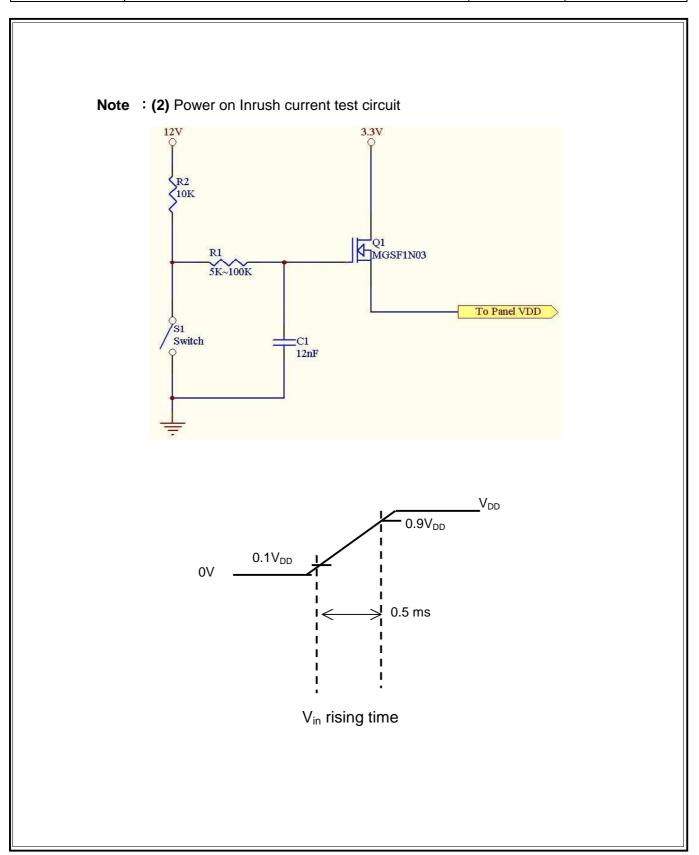
When VDD operating within $2.7V \le VDD < 3.0V$, $td \le 10ms$, the display may momentarily become abnormal.

VDD<2.7V, VDD dip condition should also follow the Power On/Off conditions for supply voltage.



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Document Title	HSD160PHW1 Specification for Wealth Star	Page No.	14/26
Document No.	DC130-004854	Revision	1.0



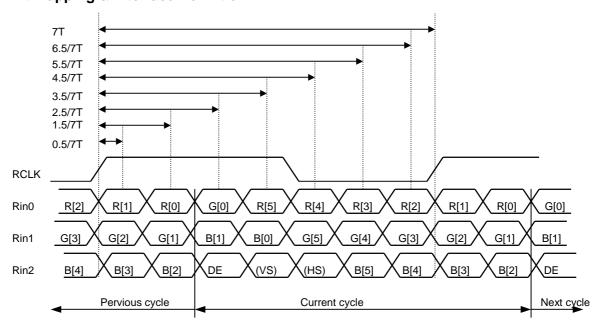


Document Title	HSD160PHW1 Specification for Wealth Star	Page No.	15/26
Document No.	DC130-004854	Revision	1.0

6.2 Switching Characteristics for LVDS Receiver

Item	Symbol	Min.	Тур.	Max.	Unit	Conditions
Differential Input High Threshold	Vth	_	_	100	mV	V _{CM} =1.2V
Differential Input Low Threshold	VtI	-100	_	_	mV	ν _{CM} =1.2ν
Input Current	I _{IN}	-10	_	+10	uA	
Differential input Voltage	$ V_{ID} $	0.1	_	0.6	V	
Common Mode Voltage Offset	V_{CM}	(V _{ID} /2)	1.25	1.8-0.4-(V _{ID} /2)	V	

6.3 Bit Mapping & Interface Definition



LVDS Receiver Input Timing Definition for 6bits LVDS input

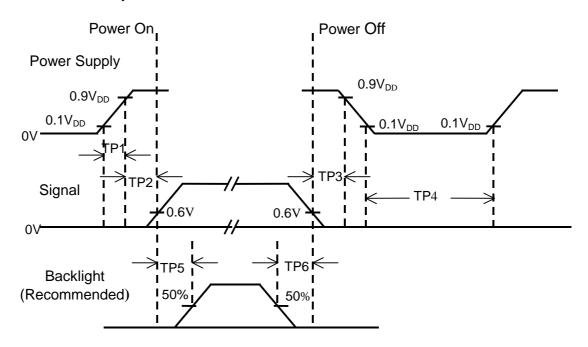
Document Title	HSD160PHW1 Specification for Wealth Star	Page No.	16/26
Document No.	DC130-004854	Revision	1.0

6.4 Interface Timing (DE	mode)					
Item	Symbol	Min.	Тур.	Max.	Unit	
Frame Rate		55	60	-	Hz	
Frame Period	t1	778	806	888	line	
Vertical Display Time	t2	768	768	768	line	
Vertical Blanking Time	t3	10	38	120	line	
1 Line Scanning Time	t4	1437	1560	1936	clock	
Horizontal Display Time	t5	1366	1366	1366	clock	
Horizontal Blanking Time	t6	71	194	570	clock	
Clock Rate	t7	50.3	75.44	85	MHz	
NCLK 13						
R,G,B[0:5] XX X,1 XX X,2 XX X,3 XX X XX						
X,767 X,768 NCLK						
1366						



Document Title	HSD160PHW1 Specification for Wealth Star	Page No.	17/26
Document No.	DC130-004854	Revision	1.0

6.5 Power On / Off Sequence



Item	Min.	Тур.	Max.	Unit	Remark
TP1	0.5	-	10	msec	
TP2	0.01	-	50	msec	
TP3	0	-	50	msec	
TP4	500			msec	
TP5	200	-	-	msec	
TP6	200			msec	

- **Note**: (1) The supply voltage of the external system for the module input should be the same as the definition of V_{DD} .
 - (2) Apply the lamp volatge within the LCD operation range. When the back-light turns on before the LCD operation or the LCD truns off before the back-light turns off, the display may momentarily become white.
 - (3) In case of VDD = off level, please keep the level of input signal on the low or keep a high impedance.
 - **(4)** TP4 should be measured after the module has been fully discharged between power off and on period.
 - (5) Interface signal shall not be kept at high impedance when the power is on.

Document Title	HSD160PHW1 Specification for Wealth Star	Page No.	18/26
Document No.	DC130-004854	Revision	1.0

6.6 Backlight Unit

Parameter	Symbol	Min	Тур	Max	Units	Condition
LED Current	I _F		15	15.45	mA	Ta=25°C
LED Voltage	V_{F}	3.0	3.2	3.33	Volt	Ta=25°C
LED Power consumption	P _{LED}		3.46	3.7	Watt	Ta=25°ℂ Note (1)
LED Life-Time	N/A	12,000			Hour	Ta=25°C
						I _{F=} 15mA Note (2)

Note (1): Calculator value for reference P=I_F x V_F x N (LED Qty')

Note (2): The LED lifetime defines as the estimated time to 50% degradation of final luminous.

6.7 LED Driver

6.7.1 Absolute Maximum Ratings

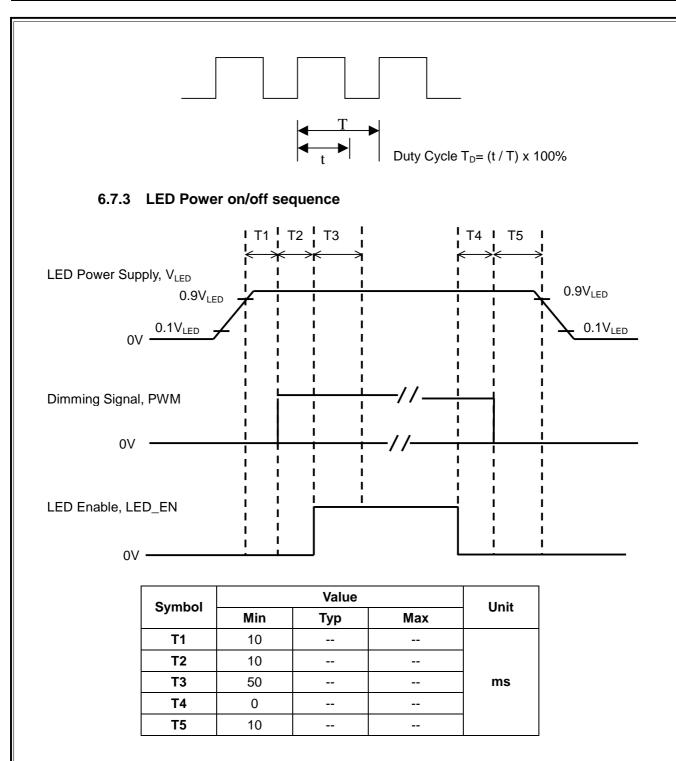
Item	Symbol	Min.	Max.	Unit	Note
LED Power Supply voltage	V_{LED}	-0.3	24	Volt	
LED_EN, PWM pin Voltage	V_{EN}, V_{PWM}		5.5	Volt	

6.7.2 DC Electrical Characteristics

Parameter	Symbol	Min	Тур	Max	Units
LED Power Supply Voltage	V_{LED}	6.0		20.0	Volt
LED_EN High Threshold	V _{ENH}	2.0			Volt
LED_EN Low Threshold	V _{ENL}			0.3	Volt
PWM High Threshold	V_{PWMH}	3.0			Volt
PWM Low Threshold	V_{PWML}			0.2	Volt
PWM Frequency	F _{PWM}	225	250	275	Hz
PWM Duty Cycle	T_D	10		100	%

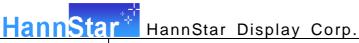


Document Title	HSD160PHW1 Specification for Wealth Star	Page No.	19/26
Document No.	DC130-004854	Revision	1.0



Note (1): The duty of LED dimming signal should be more than 20% in T2 and T3

Note (2): PWM can adjust brightness to control Pin. Pulse duty the bigger the brighter.

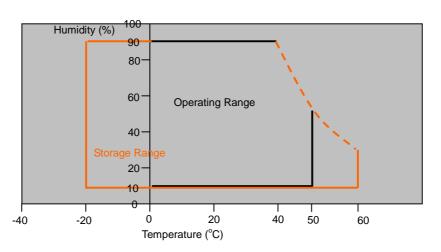


Document Title	HSD160PHW1 Specification for Wealth Star	Page No.	20/26
Document No.	DC130-004854	Revision	1.0

7.0 Reliability test items

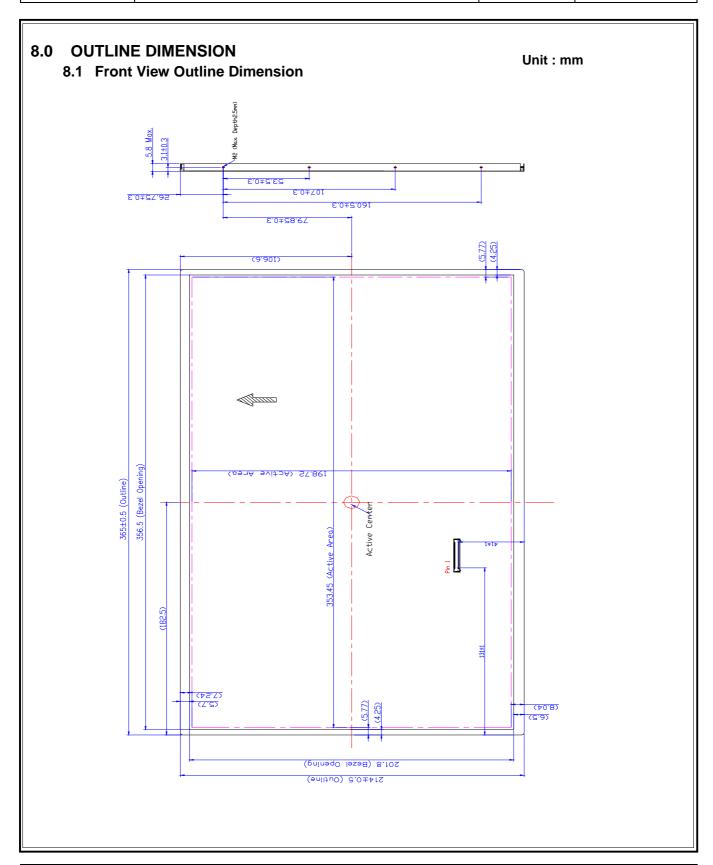
No.	Item	Conditions	Remark
1	High Temperature Storage	Ta=+60°C, 240hrs	
2	Low Temperature Storage	Ta=-20°C, 240hrs	
3	High Temperature Operation	Ta=+50°C, 500hrs	
4	Low Temperature Operation	Ta=0°C, 500hrs	
5	Thermal Cycling Test (non operation)	-20°C(30min)→+60°C(30min),100 cycles	
	Vibration	Sine Wave	
6		1.5G, 5~500Hz, XYZ	
		30min/each direction	
7	Shock	Half-Sine, 200G, 2ms, ±XYZ, 1time	

Storage / Operating temperature



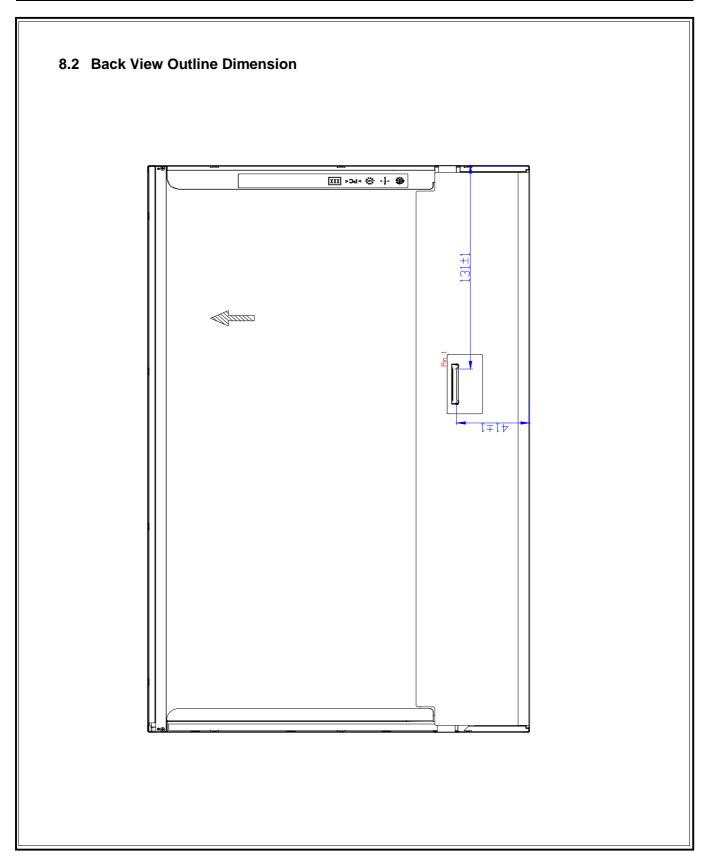
Note .Max wet bulb temp.=39°C

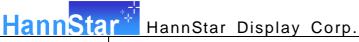
Document Title	HSD160PHW1 Specification for Wealth Star	Page No.	21/26
Document No.	DC130-004854	Revision	1.0



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Document Title	HSD160PHW1 Specification for Wealth Star	Page No.	22/26
Document No.	DC130-004854	Revision	1.0





Document Title	HSD160PHW1 Specification for Wealth Star	Page No.	23/26
Document No.	DC130-004854	Revision	1.0

9.0 LOT MARK

9.1 Lot Mark

1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

Code 1,2,3,4,5,6: HannStar internal flow control code.

Code 7: production location. Code 8: production year.

Code 9: production month.

Code 10,11,12,13,14,15: serial number.

Note (1) Production Year

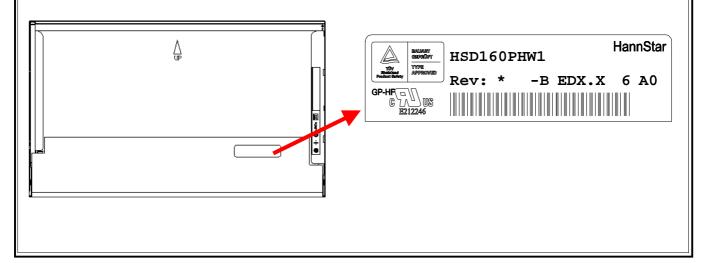
Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

Note (2) Production Month

\ /												
Month	Jan.	Feb.	Mar.	Apr.	May.	Jun.	Jul.	Aug.	Sep.	Oct	Nov.	Dec.
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

9.2 Location of Lot Mark

- (1) The label is attached to the backside of the LCD module.
- (2) This is subject to change without prior notice.





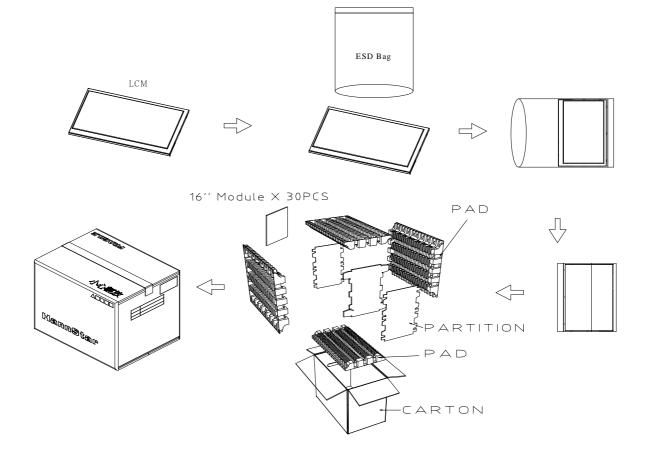
Document Title	HSD160PHW1 Specification for Wealth Star	Page No.	24/26
Document No.	DC130-004854	Revision	1.0

10.0 PACKAGE SPECIFICATION

10.1 Packing form

LCM Model	LCM Qty. in the box	Inner Box Size (mm)	Notice
HSD160PHW-B	30 pcs/box	530 x 314 x 464 ^H	

10.2 Packing assembly drawings



HSD160PHW1-B	Material	Notice
Box	Corrugated Paper Board	(AB Flute)
Partition/Pad	Corrugated Paper Board	(B Flute)
Corner Pad	Corrugated Paper Board	(AB Flute)
ESD bag	PE	

Document Title	HSD160PHW1 Specification for Wealth Star	Page No.	25/26
Document No.	DC130-004854	Revision	1.0

11.0 GENERAL PRECAUTION

11.1 Use Restriction

This product is not authorized for use in life supporting systems, aircraft navigation control systems, military systems and any other application where performance failure could be life-threatening or otherwise catastrophic.

11.2 Disassembling or Modification

Do not disassemble or modify the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display. HannStar does not warrant the module, if customers disassemble or modify the module.

11.3 Breakage of LCD Panel

- 11.3.1.If LCD panel is broken and liquid crystal spills out, do not ingest or inhale liquid crystal, and do not contact liquid crystal with skin.
- 11.3.2. If liquid crystal contacts mouth or eyes, rinse out with water immediately.
- 11.3.3. If liquid crystal contacts skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.
- 11.3.4. Handle carefully with chips of glass that may cause injury, when the glass is broken.

11.4 Electric Shock

- 11.4.1. Disconnect power supply before handling LCD module.
- 11.4.2. Do not pull or fold the LED cable.
- 11.4.3. Do not touch the parts inside LCD modules and the fluorescent LED's connector or cables in order to prevent electric shock.

11.5 Absolute Maximum Ratings and Power Protection Circuit

- 11.5.1. Do not exceed the absolute maximum rating values, such as the supply voltage variation, input voltage variation, variation in parts' parameters, environmental temperature, etc., otherwise LCD module may be damaged.
- 11.5.2. Please do not leave LCD module in the environment of high humidity and high temperature for a long time.
- 11.5.3. It's recommended to employ protection circuit for power supply.

11.6 Operation

- 11.6.1 Do not touch, push or rub the polarizer with anything harder than HB pencil lead.
- 11.6.2 Use fingerstalls of soft gloves in order to keep clean display quality, when persons handle the LCD module for incoming inspection or assembly.
- 11.6.3 When the surface is dusty, please wipe gently with absorbent cotton or other soft material.



Document Title	HSD160PHW1 Specification for Wealth Star	Page No.	26/26
Document No.	DC130-004854	Revision	1.0

- 11.6.4 Wipe off saliva or water drops as soon as possible. If saliva or water drops contact with polarizer for a long time, they may causes deformation or color fading.
- 11.6.5 When cleaning the adhesives, please use absorbent cotton wetted with a little petroleum benzine or other adequate solvent.

11.7 Mechanism

Please mount LCD module by using mounting holes arranged in four corners tightly.

11.8 Static Electricity

- 11.8.1 Protection film must remove very slowly from the surface of LCD module to prevent from electrostatic occurrence.
- 11.8.2 Because LCD module use CMOS-IC on circuit board and TFT-LCD panel, it is very weak to electrostatic discharge. Please be careful with electrostatic discharge. Persons who handle the module should be grounded through adequate methods.

11.9 Strong Light Exposure

The module shall not be exposed under strong light such as direct sunlight. Otherwise, display characteristics may be changed.

11.10 Disposal

When disposing LCD module, obey the local environmental regulations.